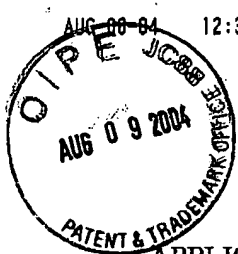


IRW Receipt



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Kevin M. Christiansen
APPLICATION NO.: 10/667,241
FILING DATE: September 18, 2003
TITLE: SYSTEM FOR DATA TRANSFER THROUGH AN I/O DEVICE
USING A MEMORY ACCESS CONTROLLER WHICH
RECEIVES AND STORES INDICATION OF A DATA STATUS
SIGNAL
EXAMINER: Unassigned
GROUP ART UNIT: 2182
ATTY. DKT. NO.: 18602-08301

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Enclosed is a copy of the Official Filing Receipt. It contains the following error:

The title is erroneously stated as:

"System for data transfer through I/O device using a memory access controller which receives and stores indication of a data status signal"

The Filing Receipt should be corrected to indicate the Title as:

"SYSTEM FOR DATA TRANSFER THROUGH AN I/O DEVICE USING A MEMORY ACCESS CONTROLLER WHICH RECEIVES AND STORES INDICATION OF A DATA STATUS SIGNAL"

18602/08301/SF/5126185.11


PATENT

The correct title is evidenced by the cover sheet of parent patent 5,961,614, a copy of which is enclosed.

Please issue a corrected Filing Receipt rectifying this error.

Respectfully submitted,
KEVIN M. CHRISTIANSEN

Dated: 6 Aug 2004.

By: 
DANIEL BROWNSTONE, Reg. No.: 46,581
Fenwick & West LLP
Silicon Valley Center
801 California Street
Mountain View, CA 94041
Tel.: (415) 875-2358
Fax.: (650) 938-5200

18602/08301/SF/5126185.12



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Applicant(s)

Kevin M. Christiansen, Saratoga, CA;

Domestic Priority data as claimed by applicant

This application is a CON of 09/972,847 10/04/2001
which is a REI of 08/647,451 05/07/1996 PAT 5,961,614
which is a CIP of 08/436,969 05/08/1995 ABN

Foreign Applications

Projected Publication Date: None, application is not eligible for pre-grant publication

Non-Publication Request: No

Early Publication Request: No

Title

an
System for data transfer through I/O device using a memory access controller which receives and stores indication of a data status signal

ATTY. DOCKET NO. 18602-301

US00059614A

United States Patent [19]
Christiansen

[11] **Patent Number:** 5,961,614
 [45] **Date of Patent:** Oct. 5, 1999

[54] **SYSTEM FOR DATA TRANSFER THROUGH AN I/O DEVICE USING A MEMORY ACCESS CONTROLLER WHICH RECEIVES AND STORES INDICATION OF A DATA STATUS SIGNAL**

[75] **Inventor:** Kevin M. Christiansen, Saratoga, Calif.

[73] **Assignee:** Apple Computer, Inc., Cupertino, Calif.

[21] **Appl. No.:** 08/647,451

[22] **Filed:** May 7, 1996

Related U.S. Application Data

[63] Continuation-in-part of application No. 08/436,969, May 8, 1995, abandoned.

[51] **Int. Cl.** G06F 13/28

[52] **U.S. Cl.** 710/22; 710/17; 710/19

[58] **Field of Search** 348/385; 371/57.1; 395/200.62; 826, 842, 845, 115, 183.2; 607/30; 364/239.51; 370/445; 710/6, 22, 24; 714/5

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Primary Examiner—Thomas C. Lee

Assistant Examiner—Chien Yuan

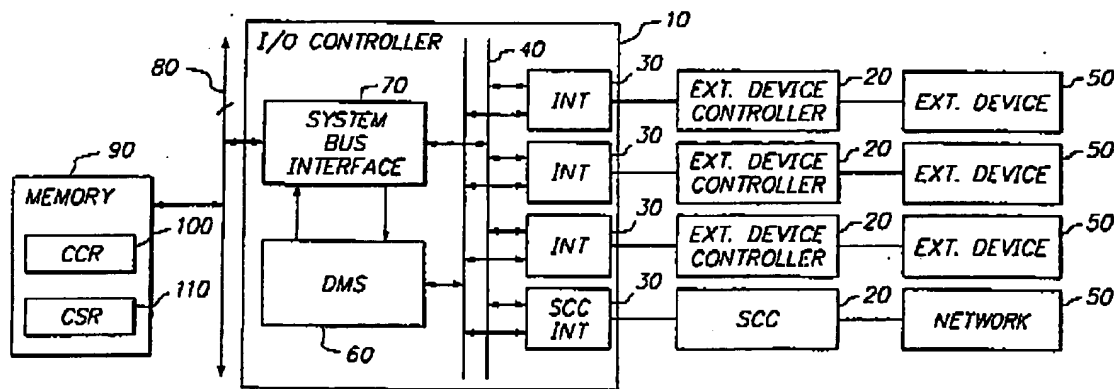
Attorney, Agent, or Firm—Burns, Doane, Swecker & Mathis, L.L.P.

[57]

ABSTRACT

A method and system for transferring units of data between a computer memory and an external system in which a DMA controller stores and uses information from an I/O device interfacing with the external system to transfer data more efficiently.

10 Claims, 7 Drawing Sheets



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			Filing Date	September 18, 2003
			First Named Inventor	Kevin M. Christiansen
			Group Art Unit Number	2182
			Examiner Name	Unassigned
Total Number of Pages in This Submission		6	Attorney Docket Number	18602-08301

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